



PATENT  
**IN THE UNITED STATES PATENT OFFICE**

Applicants:

Jason H. Anderson et al.

Assignee:

Xilinx, Inc.

Title:

Post-Layout Optimization in Integrated Circuit Design

Serial No.:

10/644,132

File Date: 08/20/2003

Examiner:

Binh C. Tat,

Art Unit: 2825

Docket No.:

X-946 US

Conf. No.: 5337

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Mail Stop AMENDMENT  
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**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicants bring to the attention of the Examiner the three (3) references listed in the attached Substitute for Form 1449A/PTO. A copy of each is enclosed herein.

The enclosed reference(s) is/are being cited after the receipt of a first office action but prior to any final action.

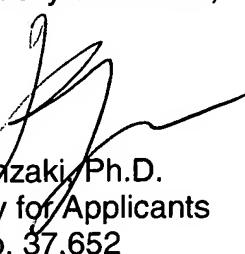
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The Commissioner is hereby authorized to charge the fee of \$180.00 due under 1.17(p) and any additional fees, or credit any overpayment which may be required to Deposit Account No. 24-0040.

Respectfully submitted,

12/23/2005 DTESEM1 00000016 240040 10644132

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I hereby certify that this correspondence is being deposited with the United States Postal Service as **first class mail** in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450, on December 19, 2005.

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<b>Complete if Known</b>				
<b>Application / Conf. No.</b>		10/644,132 / 5337		
<b>Filing Date</b>		August 20, 2003		
<b>First Named Inventor</b>		Jason H. Anderson		
<b>Art Unit</b>		2825		
<b>Examiner Name</b>		Binh C. Tat		
<b>Sheet</b>	1	of	1	Attorney Docket Number
				X-946 US

## OTHER - NON PATENT LITERATURE DOCUMENTS

Examiner Signature		Date Considered	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

**<sup>1</sup>Applicant's unique citation designation number. <sup>2</sup>Applicant is to place a check mark here if English language Translation is attached.**

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